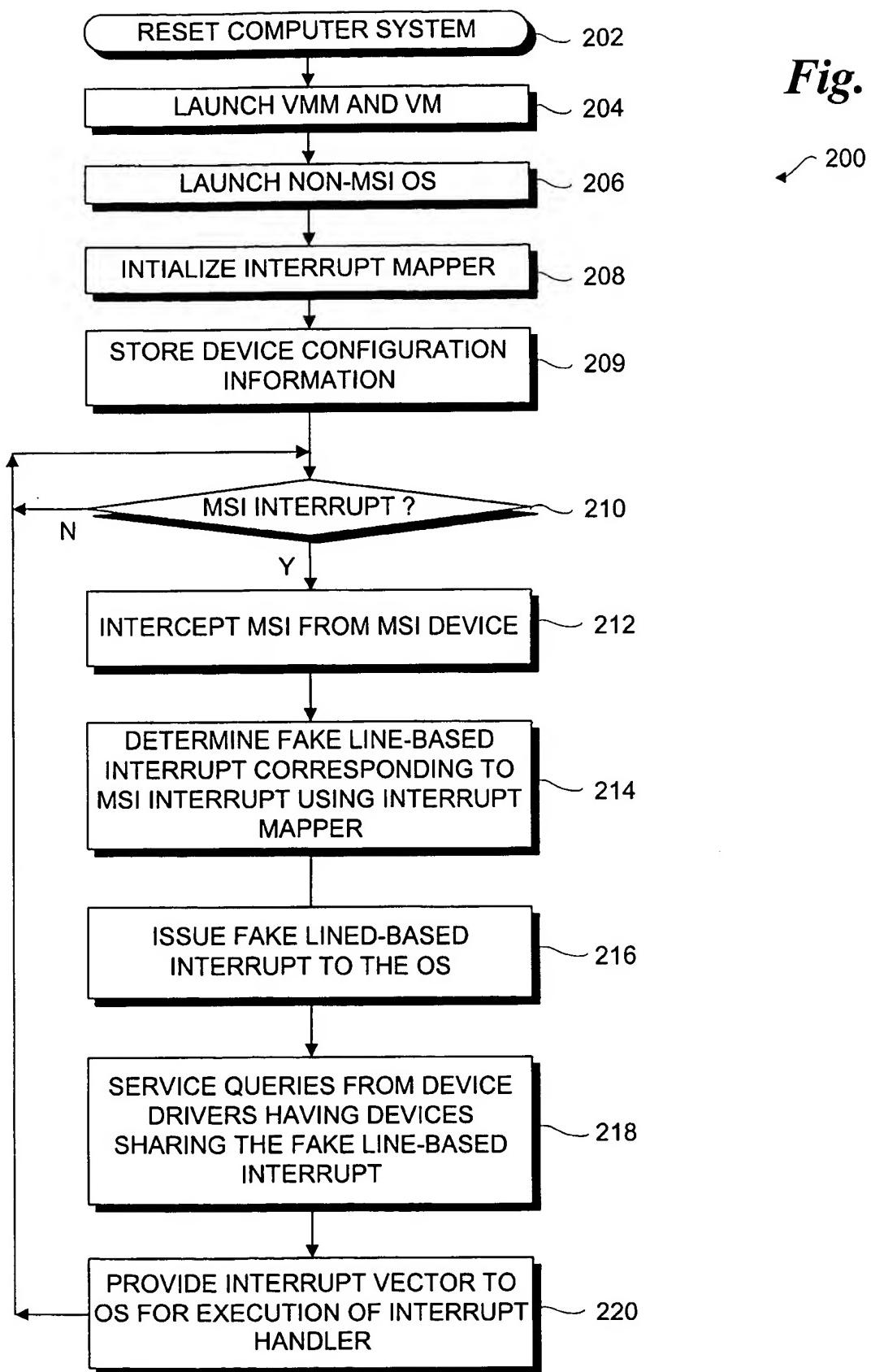
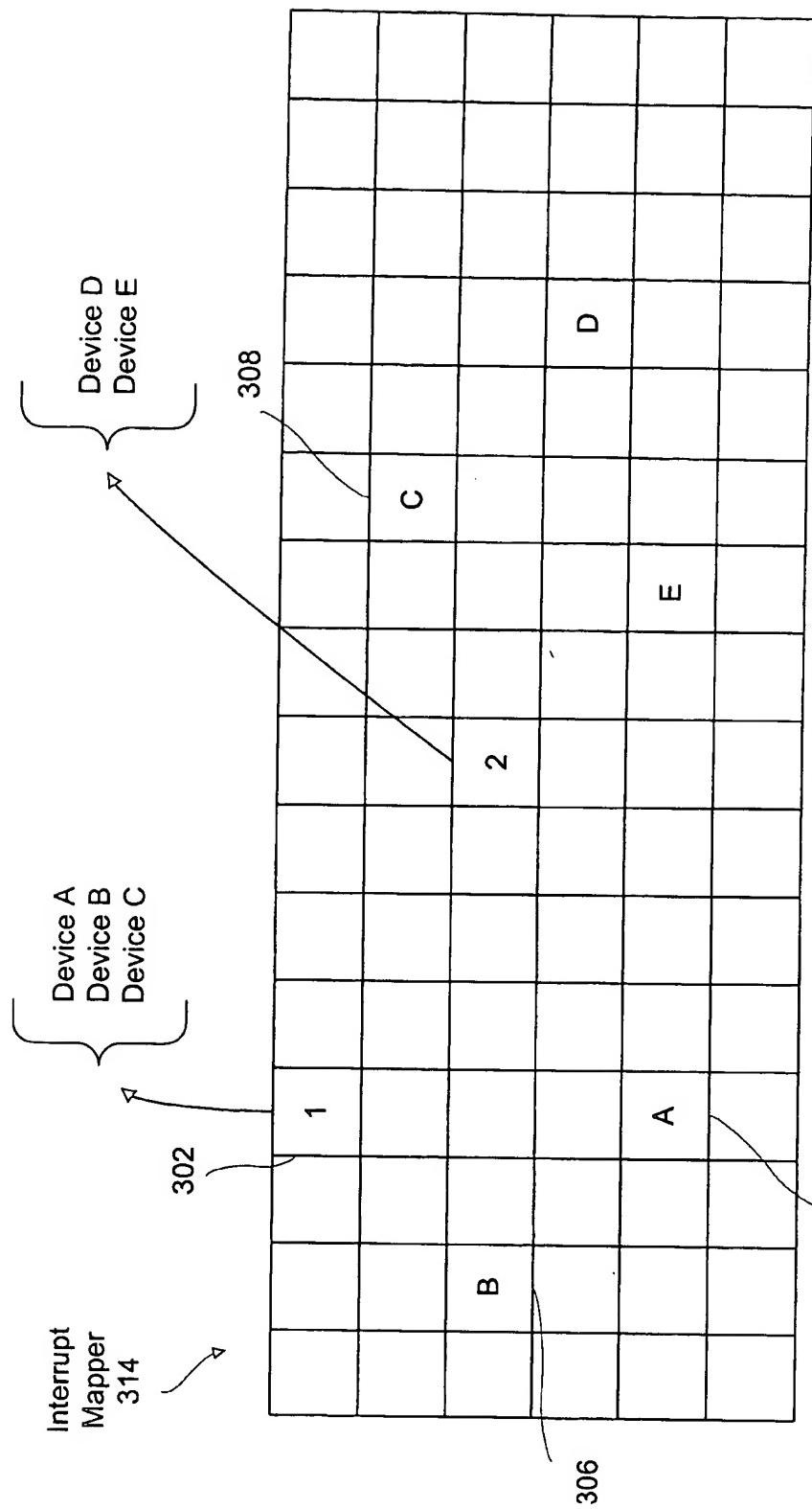


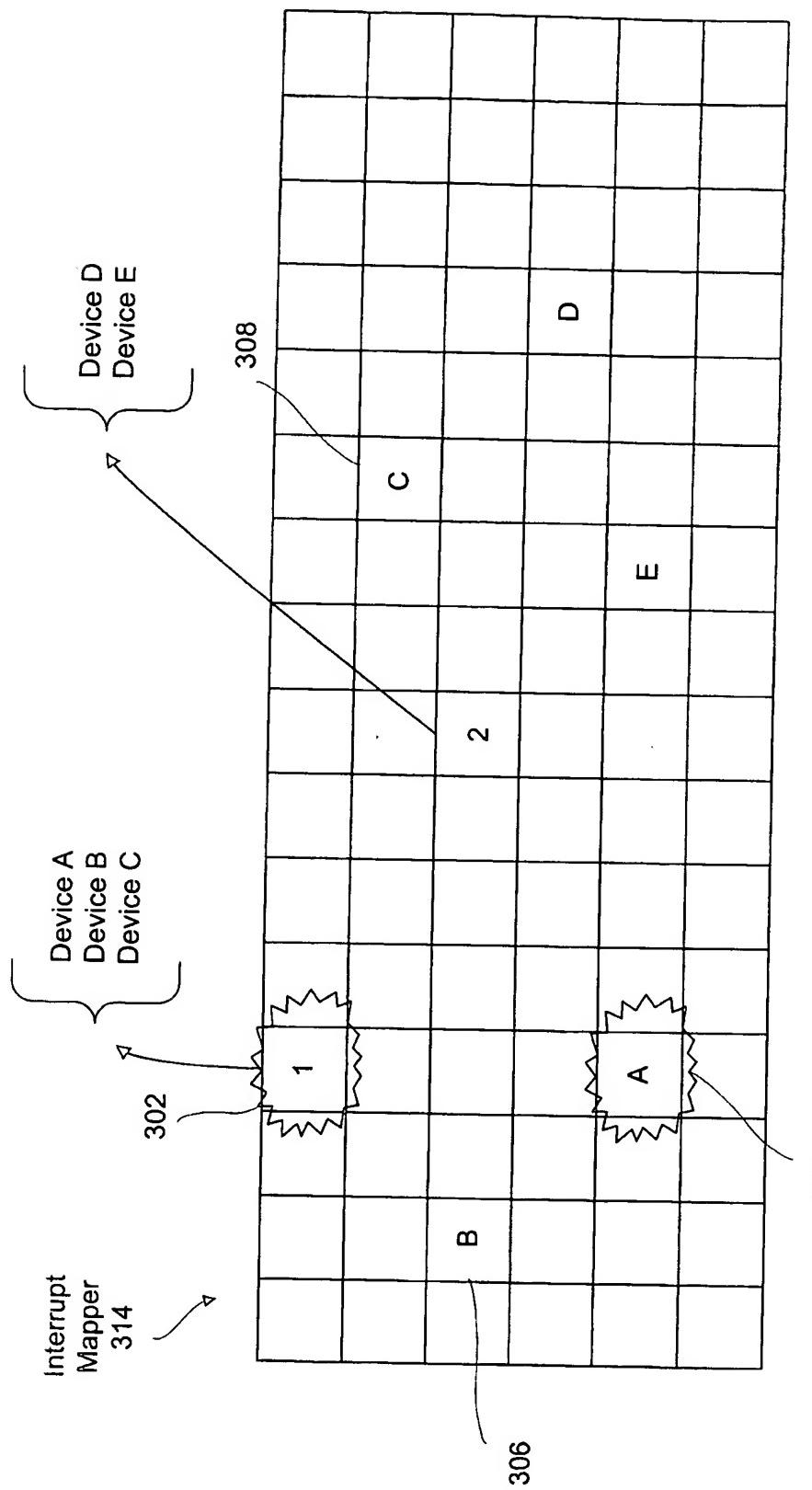
Fig. 2





Line-Based Interrupts from OS view: 1,2
MSI devices: A,B,C,D,E

Fig. 3A



Line-Based Interrupts from OS view: 1,2

MSI devices: A,B,C,D,E

Fig. 3B

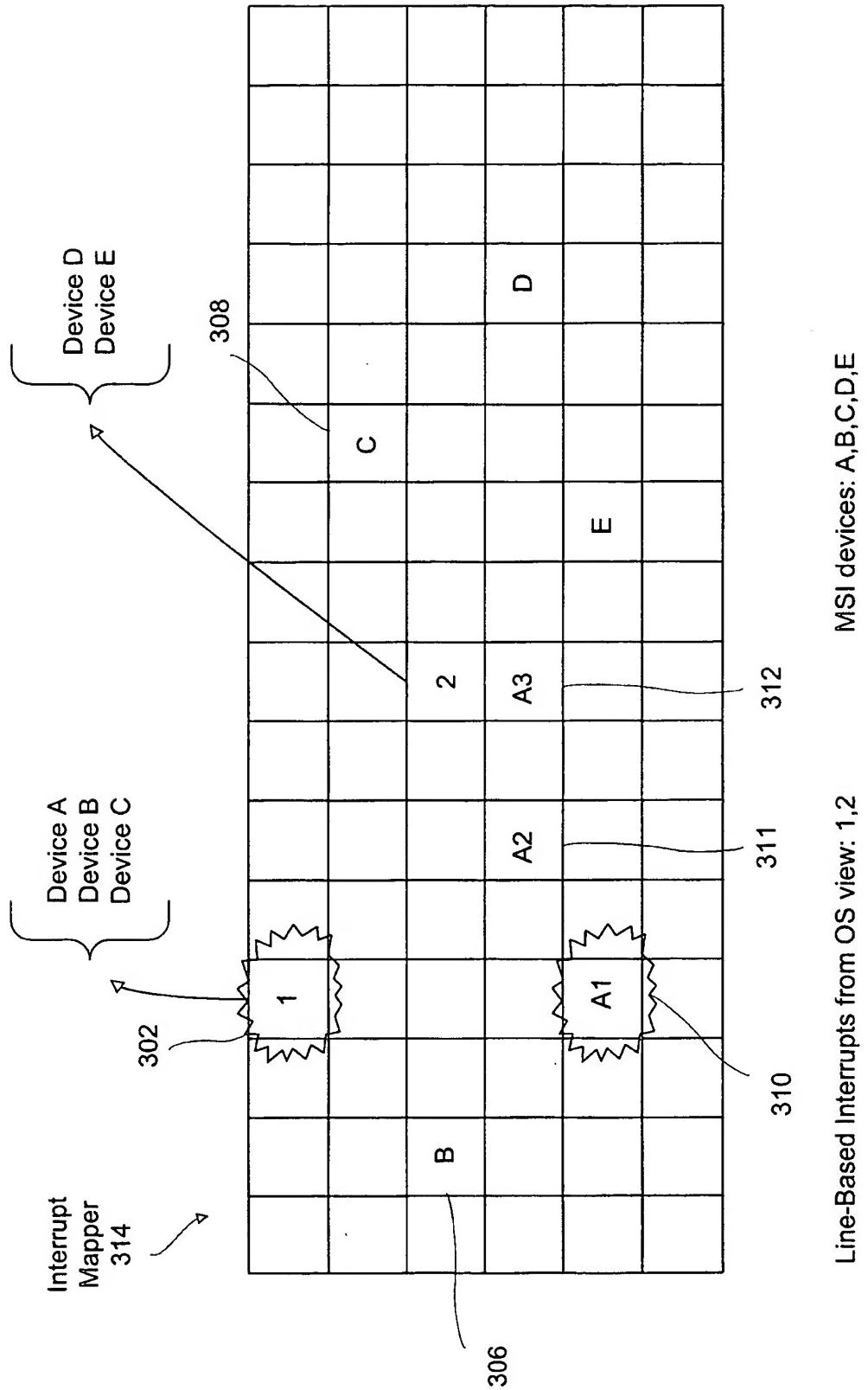
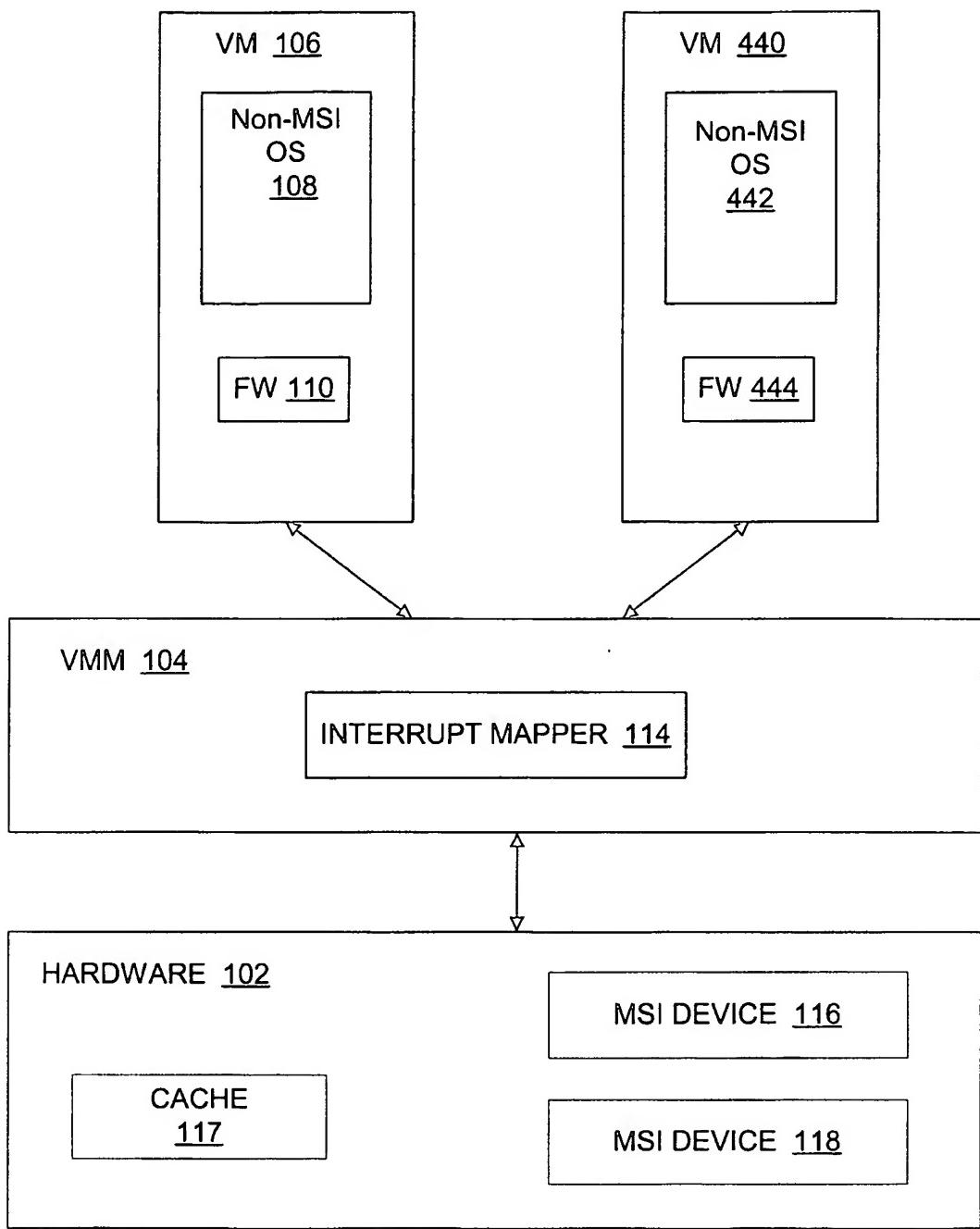


Fig. 3C



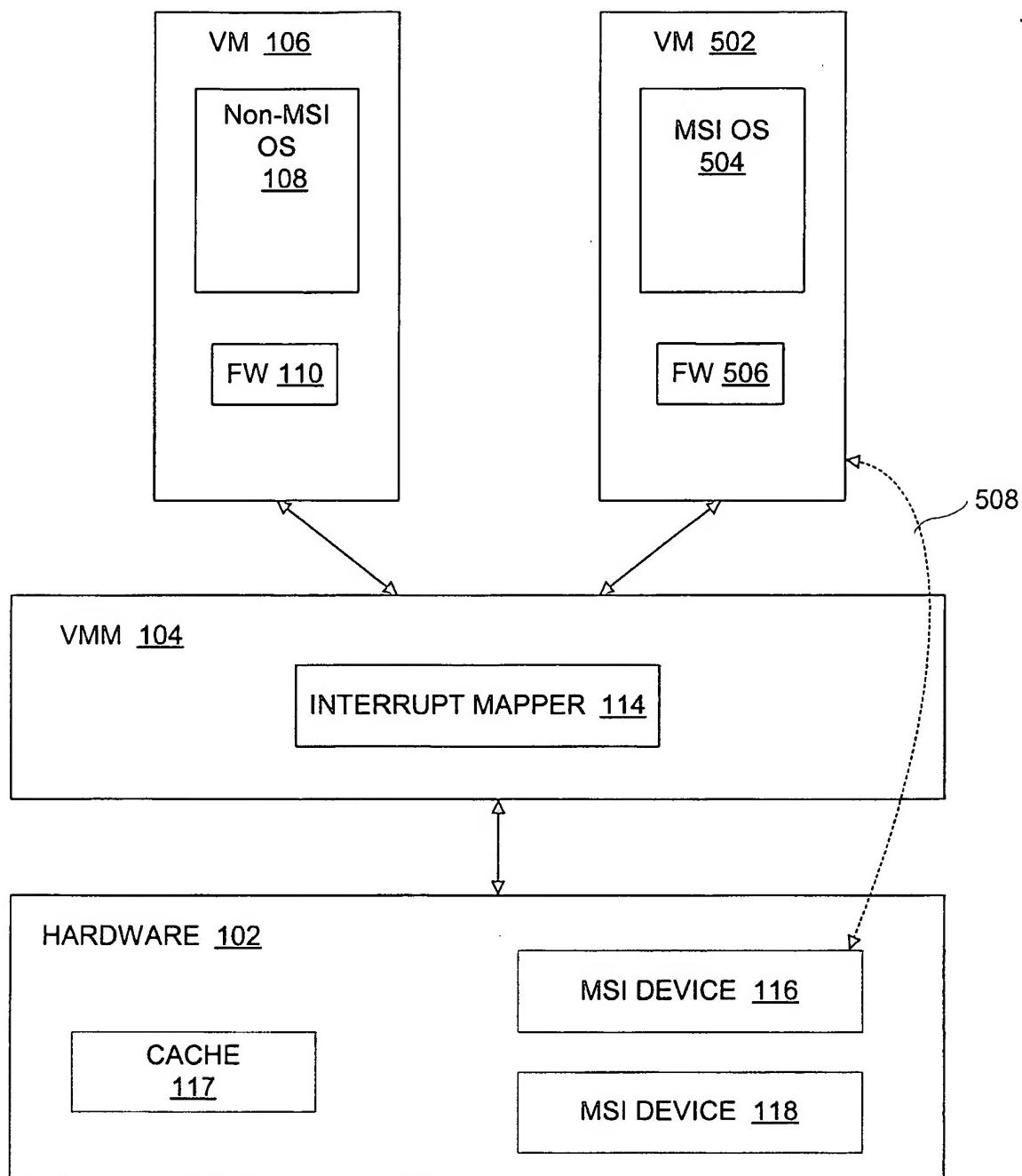


Fig. 5A

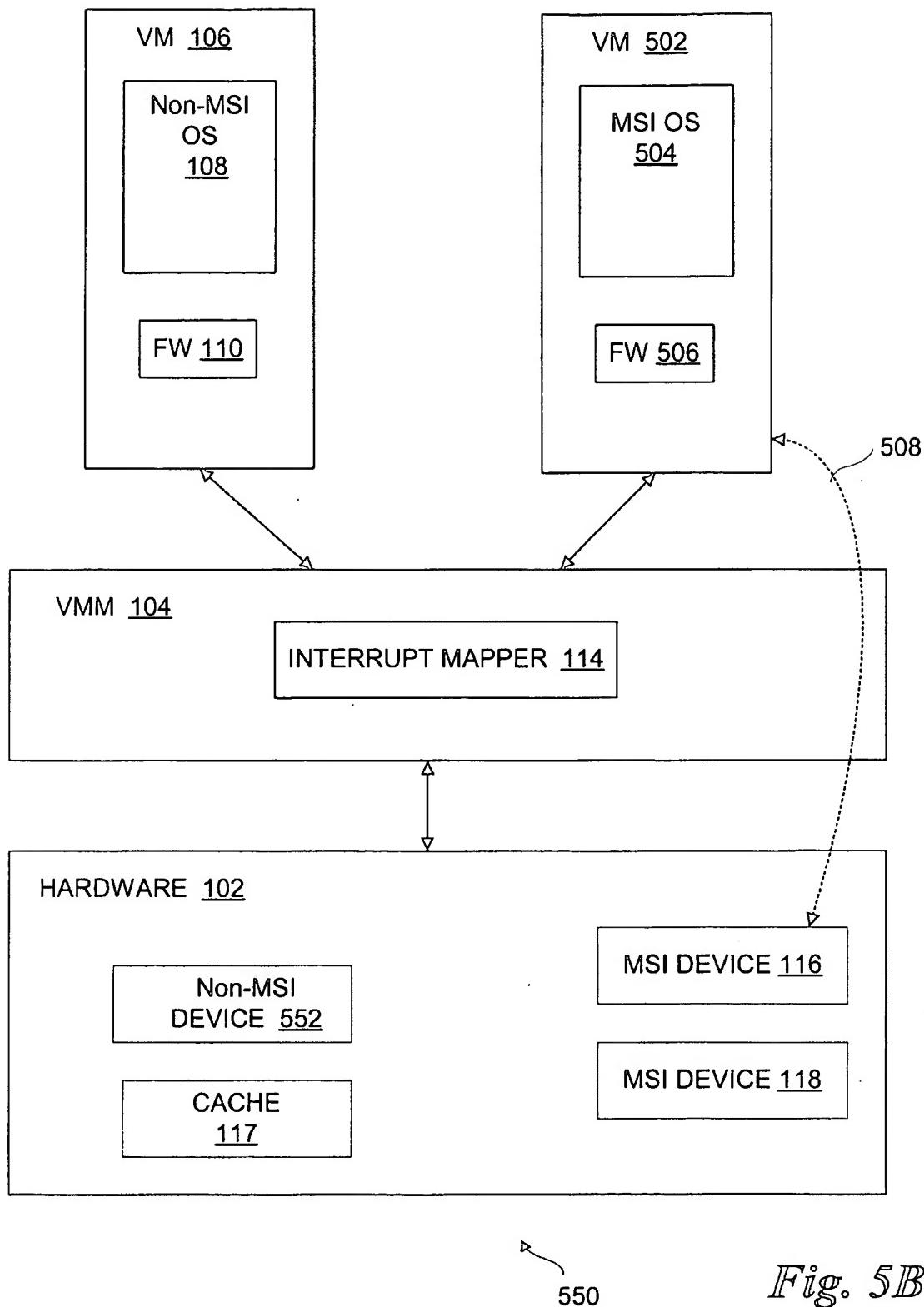


Fig. 5B

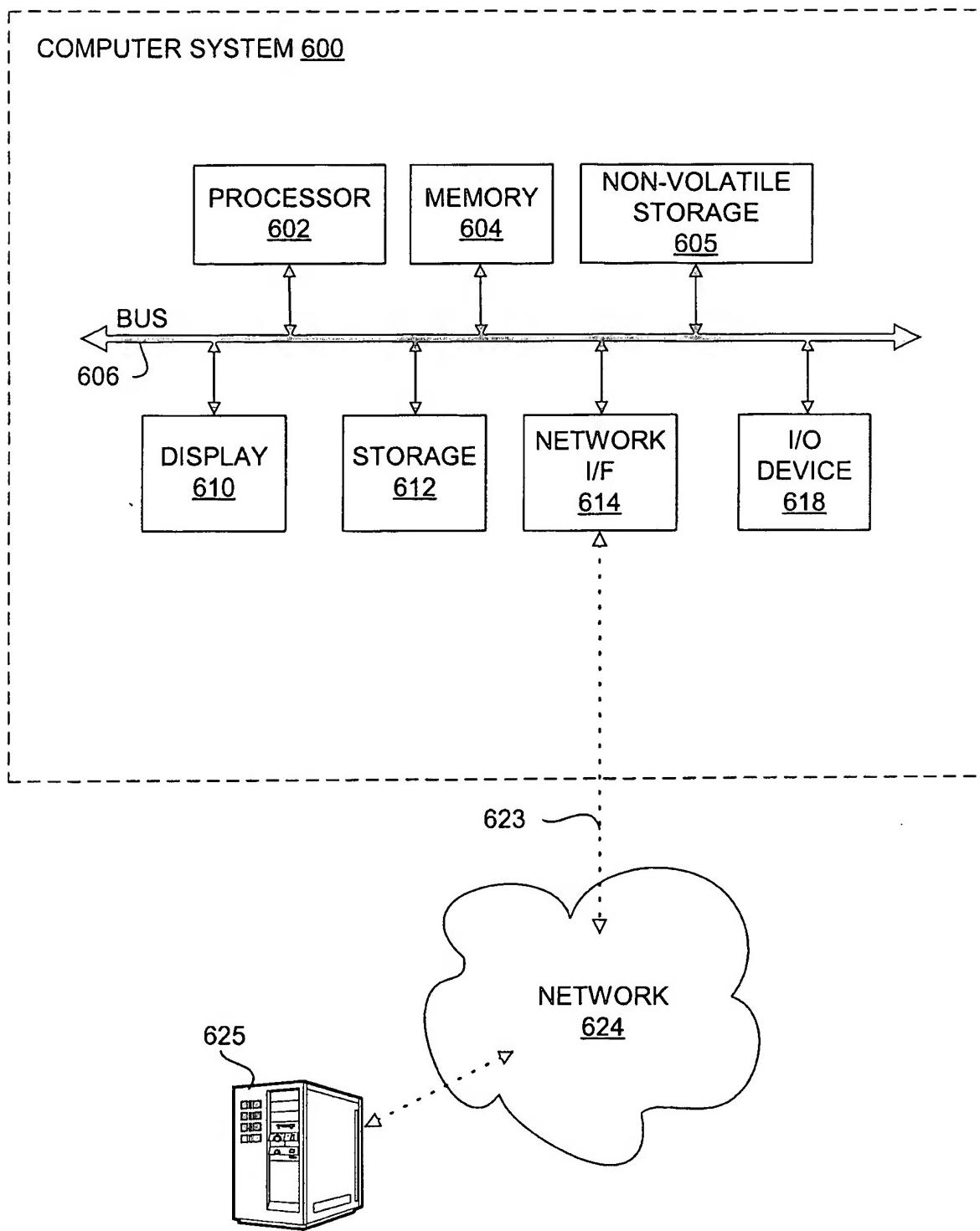


Fig. 6